A Note From Dave Miller (AA4DF) and Jill Bryant

at

http://www.aa4df.com

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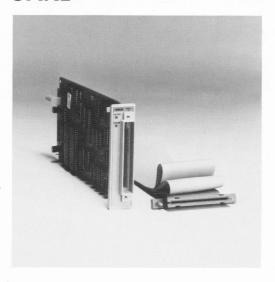
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-Dave and Jill http://www.aa4df.com



50M30 PROGRAMMABLE DIGITAL INPUT/OUTPUT CARD



REFERENCE GUIDE

GENERAL INFORMATION

Instrument Description

The 50M30 Programmable Digital Input/Output Card is a function card used in the MI 5010/MX 5010 Multifunction Interface system.

The 50M30 provides 16 digital input and 16 digital output lines. The digital inputs accept data from pushbuttons, switches, contact closures, and most digital devices capable of supplying TTL output levels. The digital outputs provide TTL levels to control various types of test and measurement instruments, relays, indicators, etc. The digital outputs can be configured for open-collector outputs by positioning internal jumpers and using power supplied by the user.

Programming of the 50M30 is via the IEEE 488 (GPIB) bus specified and described in the IEEE 488 Standard 488-1978. System commands sent to the MI 5010 microprocessor, along with specialized programming commands unique to the 50M30, control the selection of the data input/output channels and the arming/trigger functions of the card.

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Four lines at the front panel connector operate as input/output pairs to handshake data with the user's external system. One handshake pair allows the user's data source to be synchronized with the 50M30 data input register and the other handshake pair allows the user's data storage device to be synchronized with the 50M30 data output register.

NOTE

Refer to the 50M30 Instruction manual for details related to external power sources, front panel interface cabling, and input/output data synchronization.

PROGRAMMING INFORMATION

General Information

Programming the 50M30 Programmable Digital Input/Output Card for digital data transfers requires a thorough understanding of the operation and commands listed in the MI 5010/MX 5010 Instruction manual and Reference Guide. The MI 5010/MX 5010 Instruction manual contains information related to function card commands, system operating modes, message formats, message processing, IEEE 488 interface messages, basic programming examples, status bytes, and error codes.

NOTE

This reference guide contains only those commands resident in the 50M30 firmware and other information related only to digital input/output operations.

Command Format

Each command consists of a header, usually followed by an alpha or numeric argument. Query commands do not have arguments, the header contains the question mark character (?) to identify it as a query command.

Examples:

ARM SRQ; CHA 2; DAT?;

Send commands with a space between the header and argument. Additional formatting characters <CR>, <LF>, and <SP> may be added between the space and argument.

Argument Formats

Alpha arguments must be sent as listed in the command lists.

The 50M30 responds to the following numeric arguments <numbers>.

- Signed or unsigned integers, including zero.
 Unsigned integers are interpreted to be positive.
 Examples: +1, 2, 3, -1, -10.
- Signed or unsigned decimal numbers. Unsigned decimals are interpreted to be positive. Leading zeros may be deleted. Examples: -3.2, +5.0, 1.2, .8, 28.7.
- Numbers expressed in scientific notation. Examples: +1.0E-2, -1.E+1, 0.E+0.

The 50M30 sends a decimal equivalent number for query argument responses. (Error messages are integers.)

Message Delimiters

The following message delimiters are used to punctuate commands:

Delimiter	Placement
<space></space>	After header
<semicolon></semicolon>	After message unit (query or
	last command argument)

Examples:

ARM SRQ; FSET?; DAT HFFFF;

COMMANDS BY FUNCTION

NOTE

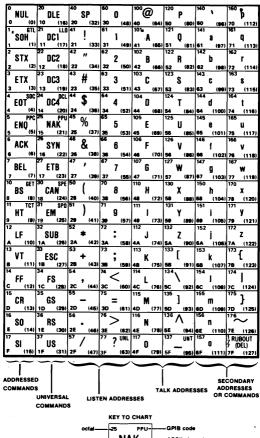
The following commands may be sent to the 50M30 in either the immediate or buffered mode. These commands will not affect the 50M30 unless it has first been selected with the proper SELECT command.

Arming Status	Channel Select
ARM ON	CHA < num>
ARM COND	CHA?
ARM SRQ	
ARM OFF	
ARM?	

Device Trigger Status	Input/Output Data Loading
DT SET	DAT <num></num>
DT TRIG	DAT B <num></num>
DT OFF	DAT H <num></num>
DT?	DAT?
	BDAT?
General Status	HDAT?
FSET?	FLAG?
NAMO	

PROGRAMMING NOTES

ASCII & IEEE 488 (GPIB) CODE CHART





3402-4

COMMAND DESCRIPTIONS			
Header	Argument(s)	Description	
ARM	ON	Handshake lines generate both a logical condition and SRQ when ODR or IDV signal occurs.	
	COND	Handshake lines generate only a logical condition when an ODR or IDV signal occurs.	
	SRQ	Handshake lines generate only an SRQ when an ODR or IDV signal occurs.	
	OFF	No action is generated by the handshake lines.	
ARM?		Armed status query.	
CHA	<num></num>	Accesses channel defined by argument. <num> = 1 for data output channel. = 2 for data input channel.</num>	
CHA?		Accessed channel query.	
DAT	<num> B<num> H<num></num></num></num>	Puts binary equivalent of <num> into Channel 1 data output register. <num> can be decimal <num>, binary B<num>, or hexadecimal H<num>.</num></num></num></num></num>	

COM Header		CRIPTIONS (cont) Description
DAT?		Decimal data format query (Channel 1 or 2).
BDAT?		Binary data format query (Channel 1 or 2).
HDAT?		Hexadecimal data format query (Channel 1 or 2).
DT	SET	Alter settings after receipt of <get> interface message or TRIG command.</get>
	TRIG	Change input/output data after receipt of <get> or TRIG command.</get>
	OFF	<get> or TRIG command has no effect.</get>
DT?		Device trigger status query.
FLAG?		Flag query for ODV or IDV interrupts.
FSET?		50M30 current settings query.
NAM?		Function card name query.

POWER-UP DEFAULTS

After the power-up self-test routine has been successfully completed, the 50M30 goes to the following default settings:

CHA 1; DAT 0;DT OFF;ARM OFF;CHA 2;ARM OFF;

NOTE

When the INIT command is sent the 50M30 returns to its power-up default settings.

STATUS BYTES AND ERROR CODES

NOTE

Refer to the MI 5010/MX 5010 Instruction manual for status byte (STB) and error code definitions.

Serial Poll Response (STB)	Error Query Response	Description
99	36x	50M30 ROM error.
225	74x	Cannot clear 50M30 PIA Data Direction Registers.
225	75x	Cannot operate 50M30 PIA Control Registers.
192+x	79x	ODR or IDV signal oc- curred with ARM SRQ or ARM ON status set.

NOTE

The value of "x" in the status byte and error code responses depends on which slot the 50M30 is located. The range of "x" is 1 through 6.

If the MI 5010 is busy when serial polled, the status byte code will be 16 higher than that listed.



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